

EAST - [10790571.wsp:1]

File View Edit Tools Window Help

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USPAT, EPO, JPO ☐ Plurals  
 Default operator: ☐ OR ☒ Highlight all hit terms initially

(memory and peripheral) and (magnetoresistive and cap and insulat\$3)

OR31... EAR... Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	R
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6754124 B2	20040622	11	Hybrid MRAM array structure and operation	365/214	365/173; 365/230.03;	
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6689622 B1	20040210	19	Magnetoresistive memory or sensor devices having improved switching	438/3		
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6593624 B2	20030715	29	Thin film transistors with vertically offset drain regions	257/344	257/324; 257/347;	
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6113642 A	20000905	34	Computer controlled hydraulic resistance device for a prosthesis and other	623/24	188/282.3; 623/44	